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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/094,512 03/07/2002 PAT 6,751,113 *yes LP*

** FOREIGN APPLICATIONS ***** *m LP*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 21	INDEPENDENT CLAIMS 3
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>				

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TITLE
 Arrangement of integrated circuits in a memory module

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